

## ESD54211N

**1-Line, Bi-directional, Transient Voltage Suppressors**

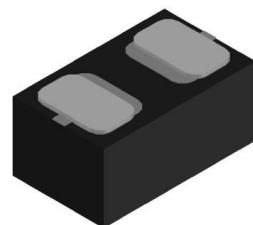
<http://www.sh-willsemi.com>

### Descriptions

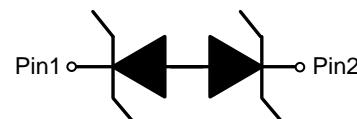
The ESD54211N is a bi-directional TVS (Transient Voltage Suppressor). It is specifically designed to protect sensitive electronic components which are connected to power lines, low speed data lines and control lines from over-stress caused by ESD (Electrostatic Discharge), EFT (Electrical Fast Transients) and Lightning.

The ESD54211N may be used to provide ESD protection up to  $\pm 30\text{kV}$  (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 10A (8/20 $\mu\text{s}$ ) according to IEC61000-4-5.

The ESD54211N is available in WBFBP-02C-C package. Standard products are Pb-free and Halogen-free.



**WBFBP-02C-C**



**Circuit diagram**



E = Device code

\* = Month code ( A~Z )

**Marking (Top View)**

### Features

- Stand-off voltage:  $\pm 3.3\text{V}$  Max
- Transient protection for each line according to  
IEC61000-4-2 (ESD):  $\pm 30\text{kV}$  (contact discharge)  
IEC61000-4-4 (EFT): 40A (5/50ns)  
IEC61000-4-5 (surge): 10A (8/20 $\mu\text{s}$ )
- Capacitance:  $C_J = 17.5\text{pF}$  typ.
- Low leakage current:  $I_R < 1\text{nA}$  typ.
- Low clamping voltage:  $V_{CL} = 9\text{V}$  typ. @  $I_{PP} = 16\text{A}$  (TLP)
- Solid-state silicon technology

### Applications

- Cellular handsets
- Computers and peripherals
- Microprocessors
- Power lines
- Portable Electronics
- Notebooks

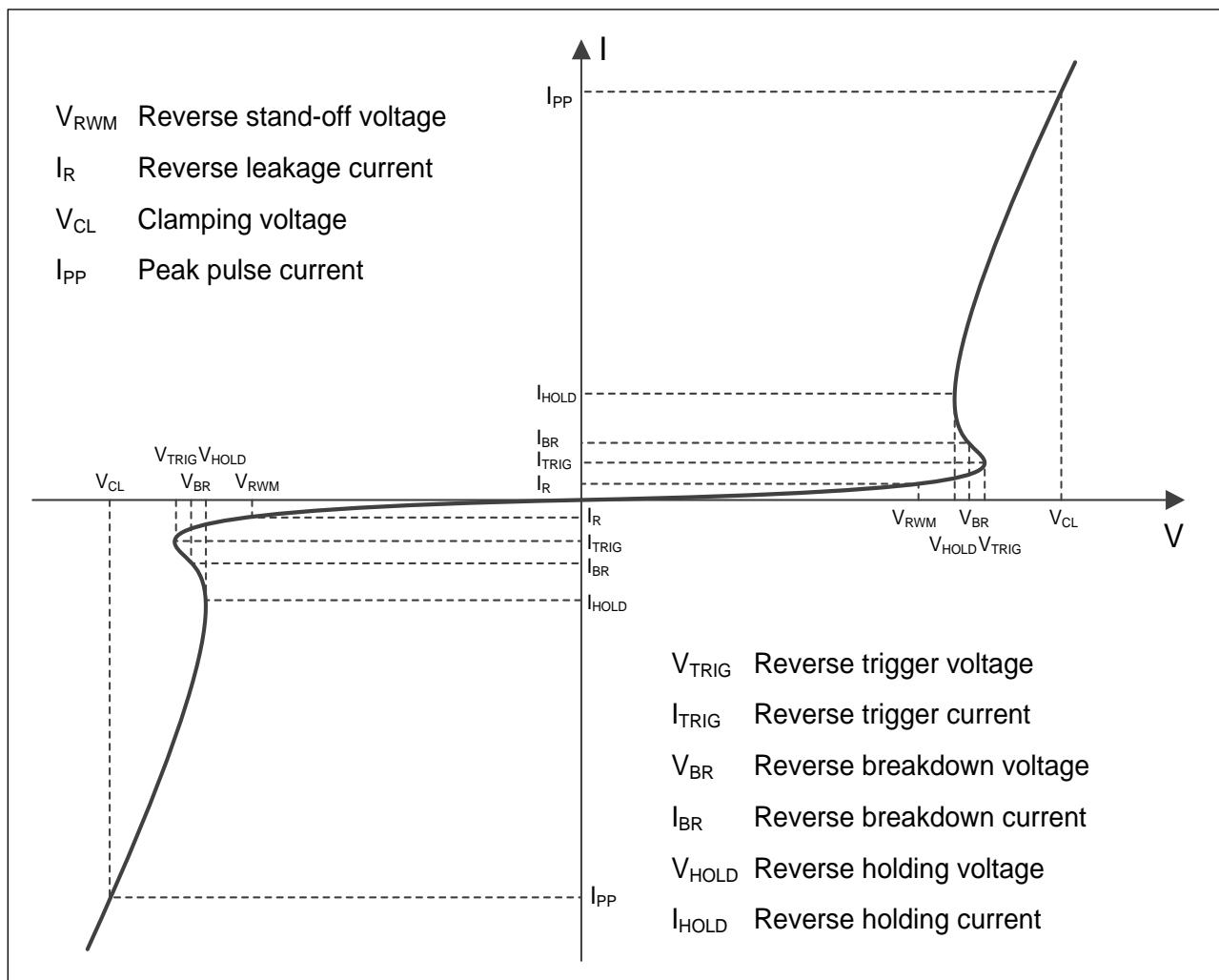
### Order information

Device	Package	Shipping
ESD54211N-2/TR	WBFBP-02C-C	10000/Tape&Reel

## Absolute maximum ratings

Parameter	Symbol	Rating	Unit
Peak pulse power ( $t_p = 8/20\mu s$ )	$P_{pk}$	120	W
Peak pulse current ( $t_p = 8/20\mu s$ )	$I_{PP}$	10	A
ESD according to IEC61000-4-2 air discharge	$V_{ESD}$	$\pm 30$	kV
ESD according to IEC61000-4-2 contact discharge		$\pm 30$	
Junction temperature	$T_J$	125	$^{\circ}C$
Operating temperature	$T_{OP}$	-40~85	$^{\circ}C$
Lead temperature	$T_L$	260	$^{\circ}C$
Storage temperature	$T_{STG}$	-55~150	$^{\circ}C$

## Electrical characteristics ( $T_A=25^{\circ}C$ , unless otherwise noted)



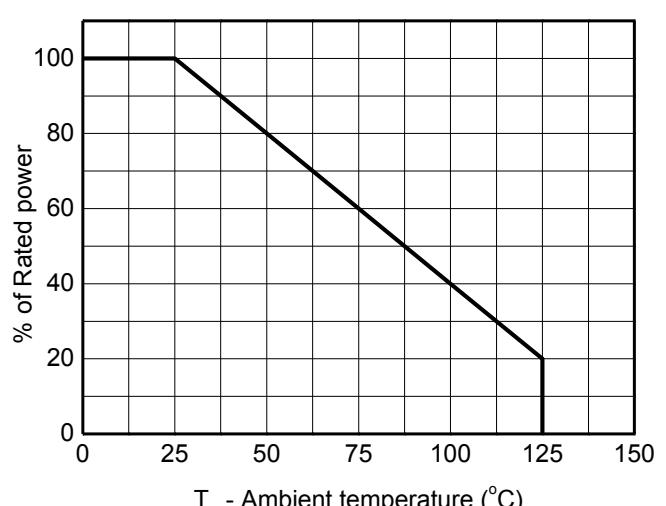
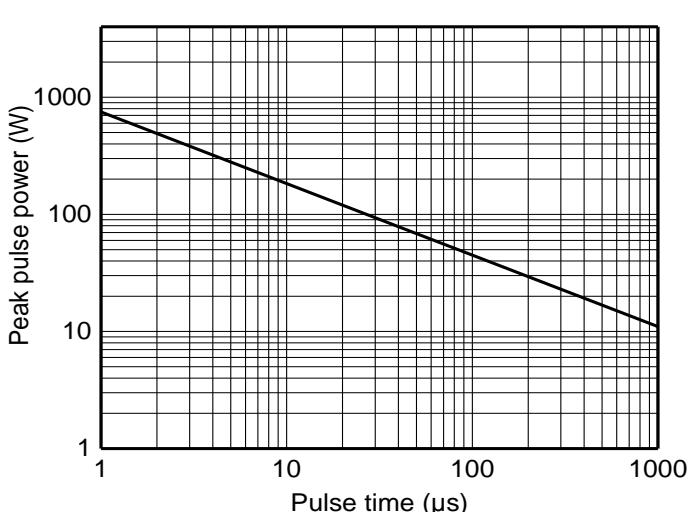
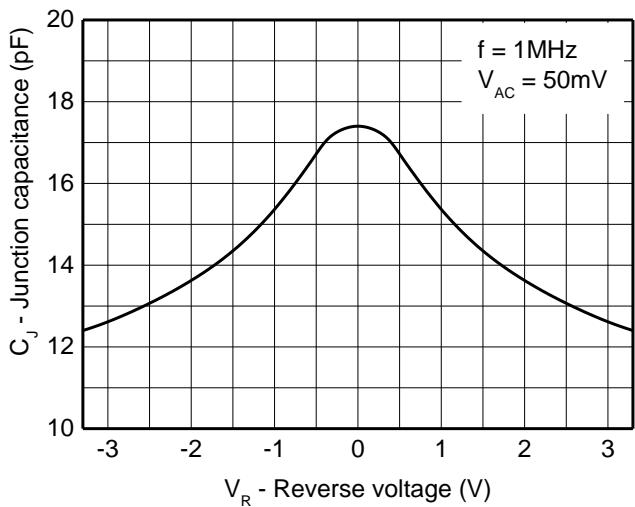
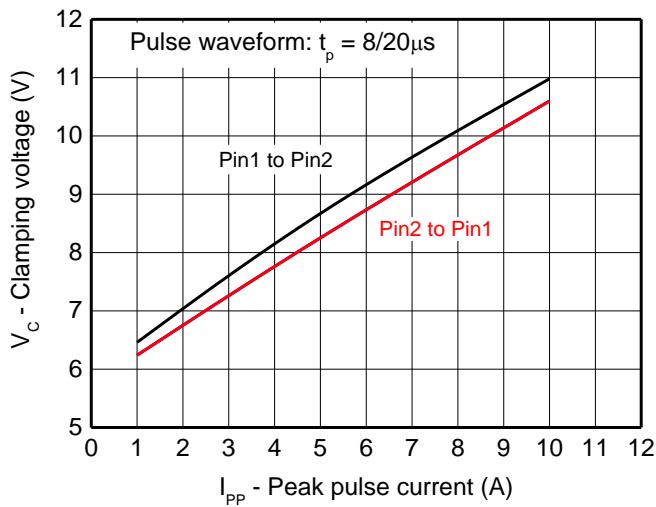
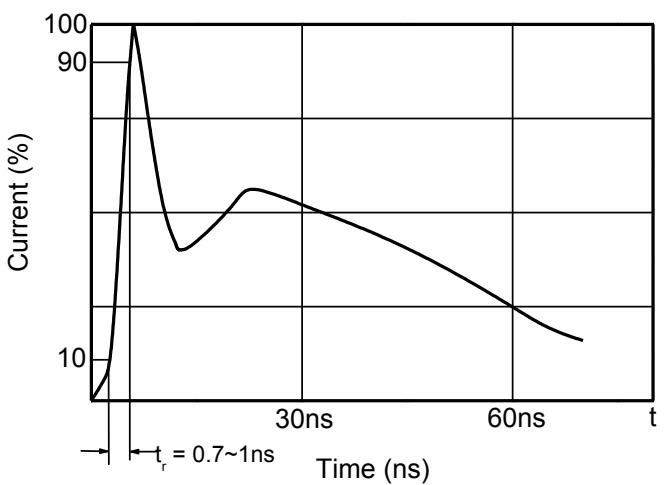
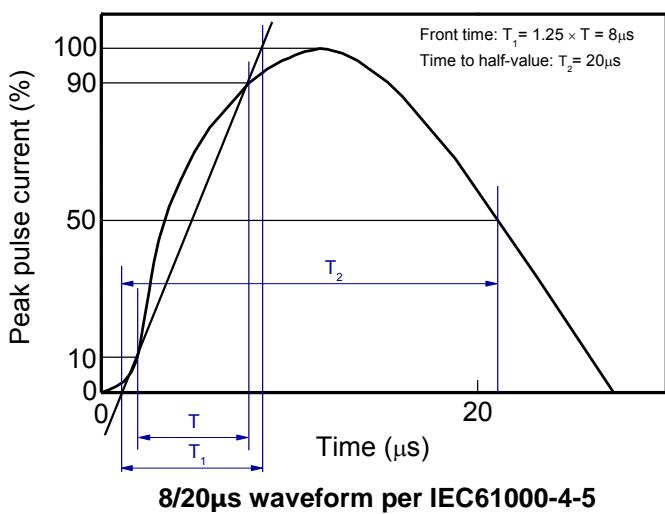
## Definitions of electrical characteristics

**Electrical characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)**

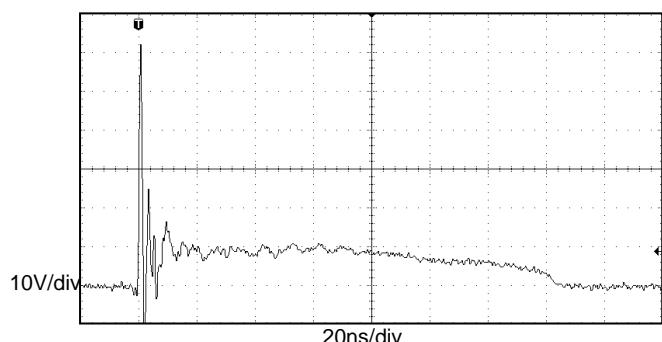
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				$\pm 3.3$	V
Reverse leakage current	$I_R$	$V_{RWM} = 3.3\text{V}$		<1	100	nA
Reverse breakdown voltage	$V_{BR}$	$I_{BR} = 1\text{mA}$	4.2			V
Reverse holding voltage	$V_{HOLD}$	$I_{HOLD} = 50\text{mA}$	4.1			V
Clamping voltage <sup>1)</sup>	$V_{CL}$	$I_{PP} = 16\text{A}, t_p = 100\text{ns}$		9		V
Clamping voltage <sup>2)</sup>	$V_{CL}$	$V_{ESD} = 8\text{kV}$		10		V
Clamping voltage <sup>3)</sup>	$V_{CL}$	$I_{PP} = 1\text{A}, t_p = 8/20\mu\text{s}$			7.5	V
		$I_{PP} = 5\text{A}, t_p = 8/20\mu\text{s}$			10	V
		$I_{PP} = 10\text{A}, t_p = 8/20\mu\text{s}$			12	V
Dynamic resistance <sup>1)</sup>	$R_{DYN}$			0.25		$\Omega$
Junction capacitance	$C_J$	$V_R = 0\text{V}, f = 1\text{MHz}$		17.5	22	pF
		$V_R = 3.3\text{V}, f = 1\text{MHz}$		12.5	16	pF

Notes:

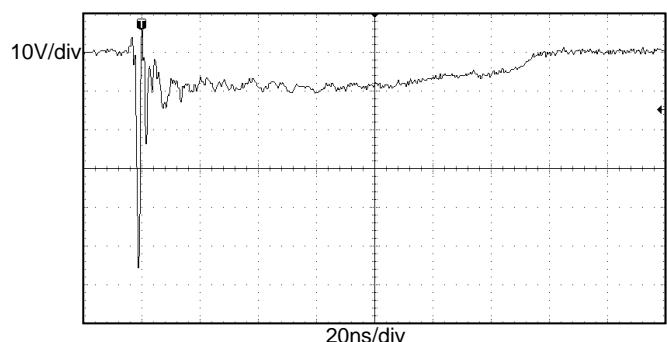
- 1) TLP parameter:  $Z_0 = 50\Omega$ ,  $t_p = 100\text{ns}$ ,  $t_r = 2\text{ns}$ , averaging window from 60ns to 80ns.  $R_{DYN}$  is calculated from 4A to 16A.
- 2) Contact discharge mode, according to IEC61000-4-2.
- 3) Non-repetitive current pulse, according to IEC61000-4-5.

**Typical characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)**


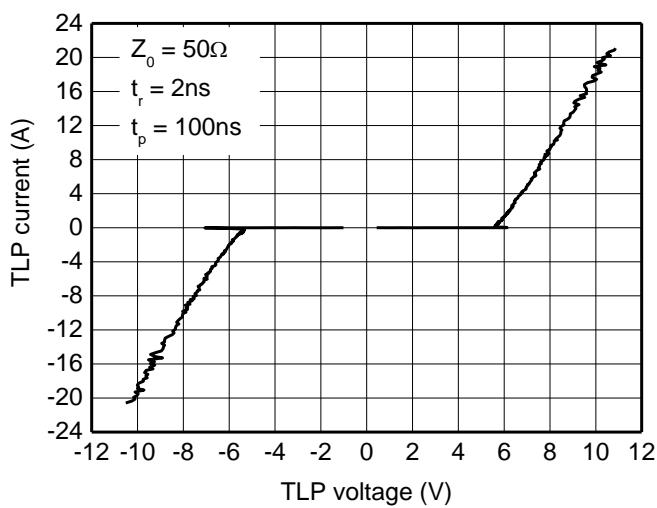
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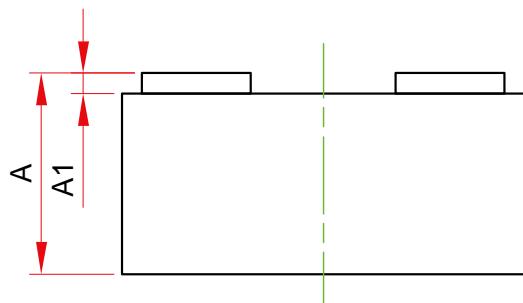
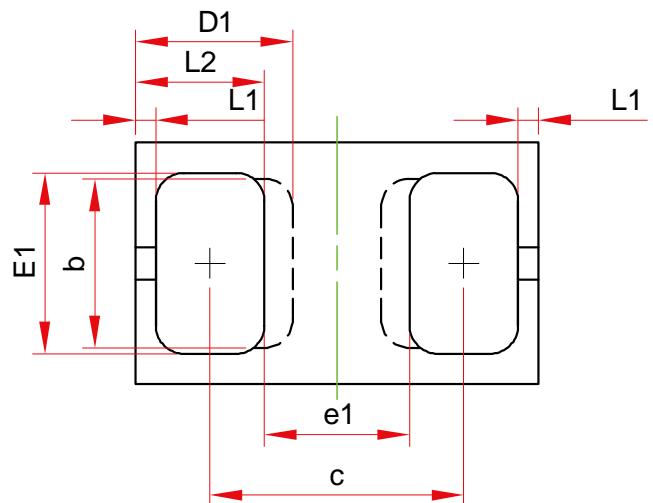
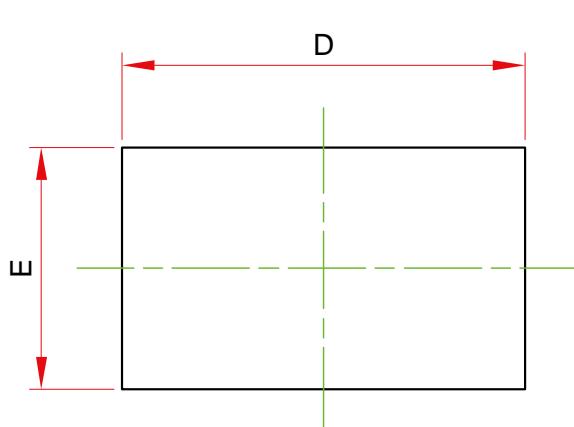
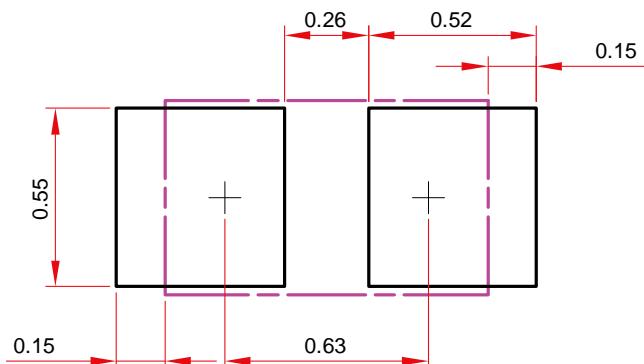
**ESD clamping**  
(+8kV contact discharge per IEC61000-4-2)



**ESD clamping**  
(-8kV contact discharge per IEC61000-4-2)



**TLP Measurement**

**Package outline dimensions**
**WBFBP-02C-C**

**Recommend land pattern (Unit: mm)**


Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.450	--	0.550
A1	0.010	--	0.090
D	0.950	--	1.050
E	0.550	--	0.650
D1	0.390 Ref.		
E1	0.400	--	0.500
b	0.420 Ref.		
c	0.580	--	0.680
e1	0.360 Ref.		
L1	0.050 Ref.		
L2	0.270	--	0.370

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.