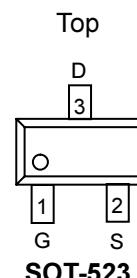


WNM4153

N-Channel, 20V, 0.88A, Small Signal MOSFET

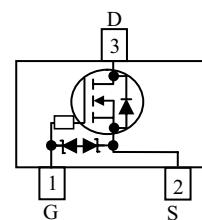
[Http://www.willsemi.com](http://www.willsemi.com)

V_{DS} (V)	R_{DS(on)} (Ω)
20	0.220 @ V _{GS} =4.5V
	0.260 @ V _{GS} =2.5V
	0.320 @ V _{GS} =1.8V



Descriptions

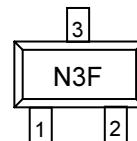
The WNM4153 is the N-Channel enhancement MOS Field Effect Transistor, uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in DC-DC conversion applications. Standard Product WNM4153 is Pb-free.



Pin Configuration

Features

- Trench N-Channel
- Supper high density cell design for extremely low R_{DS(on)}
- Exceptional ON resistance and maximum DC current capability
- Small package design with SOT-523



N3 = Device Code

F = Month

Marking

Applications

- Driver: Relays, Solenoids, Lamps, Hammers
- Power supply converters circuit
- Load/Power Switching for potable device

Order Information

Device	Package	Shipping
WNM4153-3/TR	SOT-523	3000/Tape&Reel

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	10 S	Steady State	Unit
Drain-Source Voltage	V_{DS}	+20		V
Gate-Source Voltage	V_{GS}	± 6		
Continuous Drain Current ^a	$T_A=25^\circ\text{C}$	I_D	0.88	A
	$T_A=70^\circ\text{C}$		0.71	
Maximum Power Dissipation ^a	$T_A=25^\circ\text{C}$	P_D	0.37	W
	$T_A=70^\circ\text{C}$		0.23	
Continuous Drain Current ^b	$T_A=25^\circ\text{C}$	I_D	0.76	A
	$T_A=70^\circ\text{C}$		0.60	
Maximum Power Dissipation ^b	$T_A=25^\circ\text{C}$	P_D	0.27	W
	$T_A=70^\circ\text{C}$		0.17	
Pulsed Drain Current ^c	I_{DM}	1.4		A
Operating Junction Temperature	T_J	150		$^\circ\text{C}$
Lead Temperature	T_L	260		$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to 150		$^\circ\text{C}$

Thermal Resistance Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

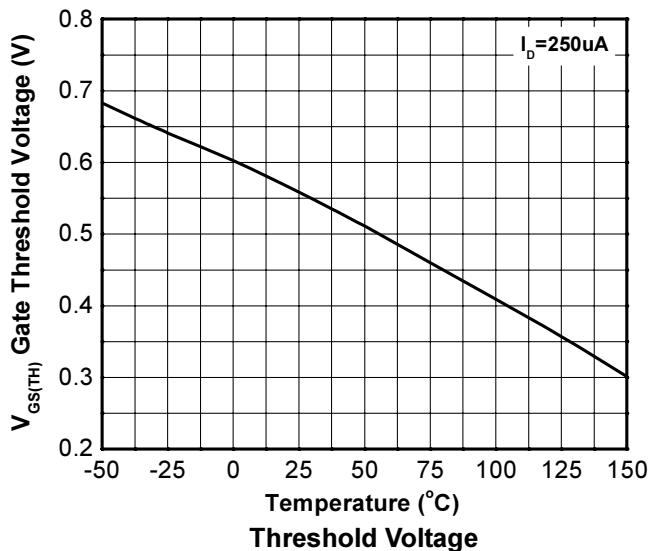
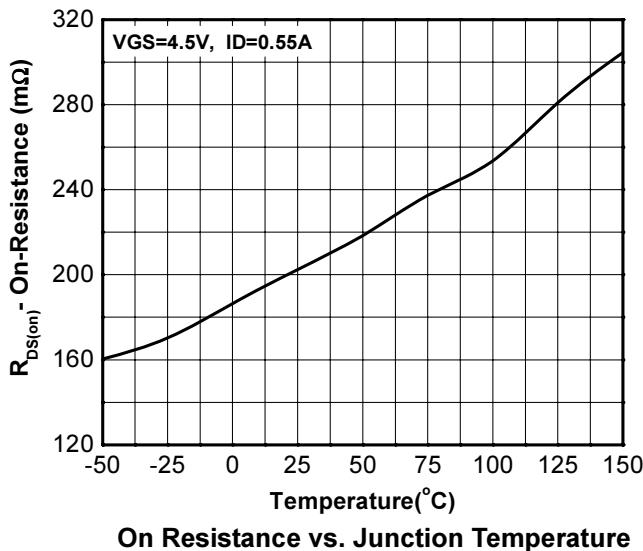
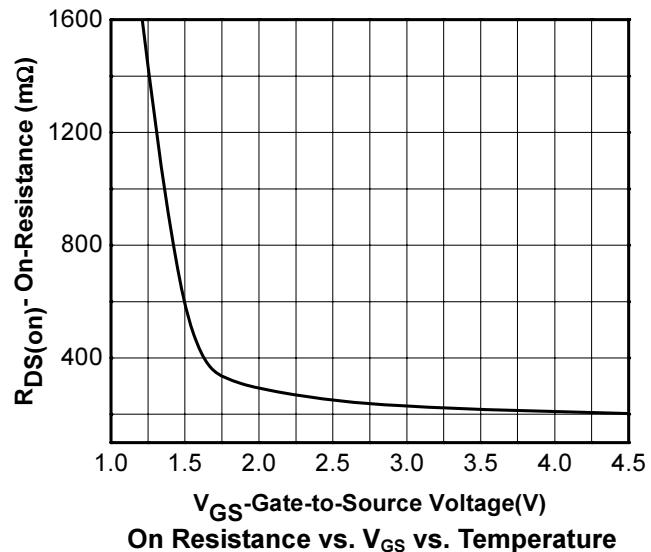
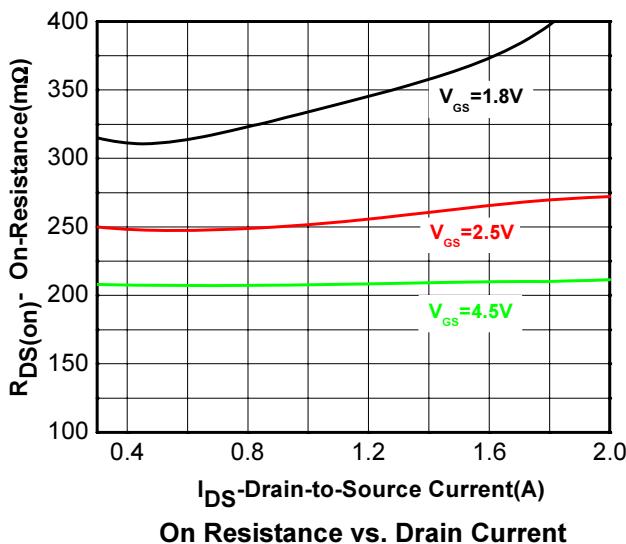
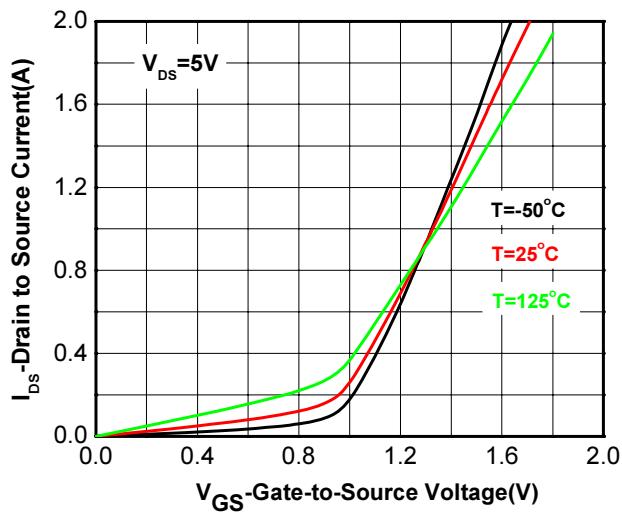
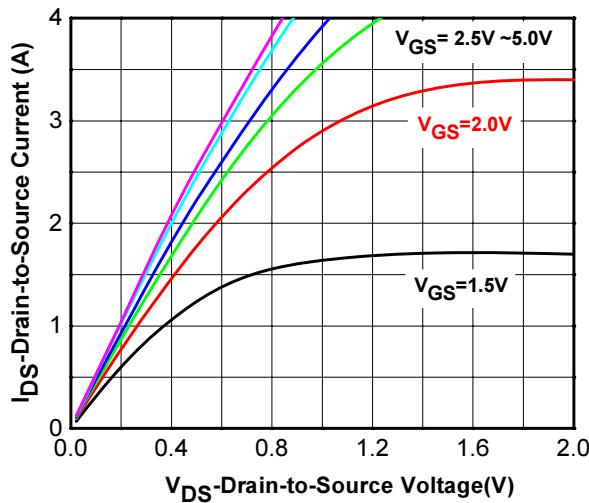
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^a	$t \leq 10\text{ s}$	$R_{\theta JA}$	285	$^\circ\text{C}/\text{W}$
	Steady State		340	
Junction-to-Ambient Thermal Resistance ^b	$t \leq 10\text{ s}$	$R_{\theta JA}$	385	$^\circ\text{C}/\text{W}$
	Steady State		455	
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	260	300	

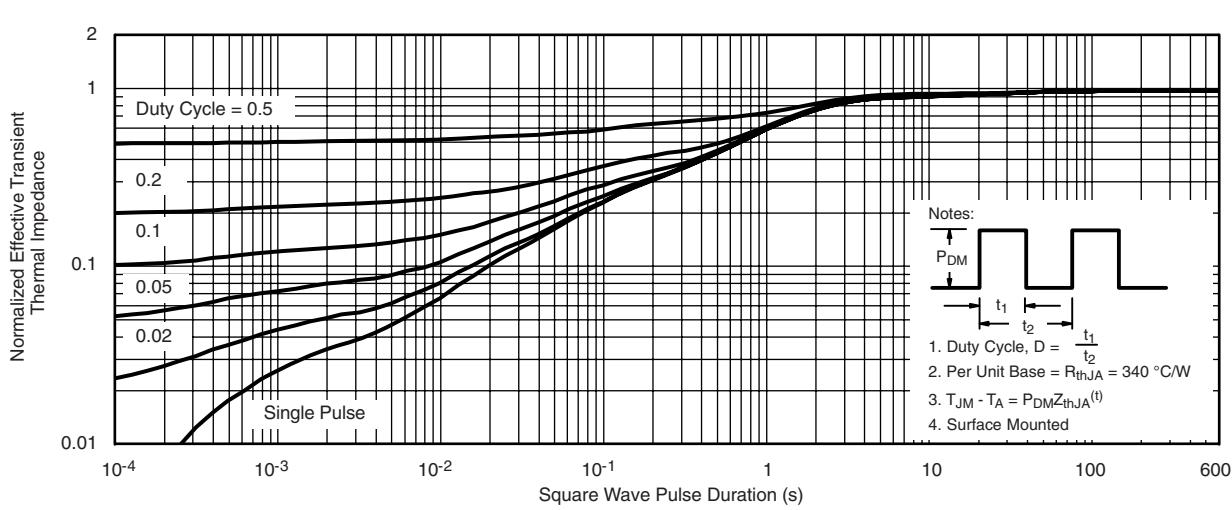
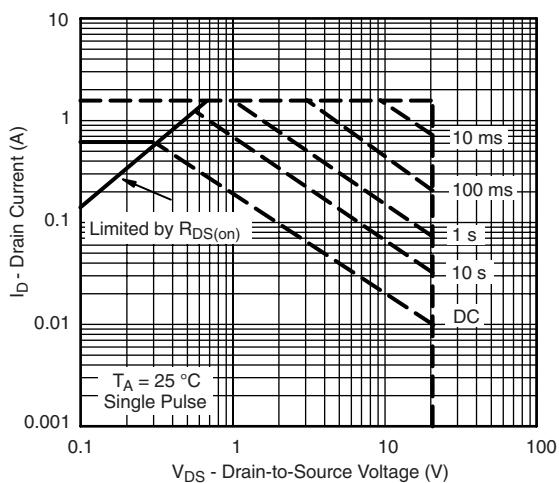
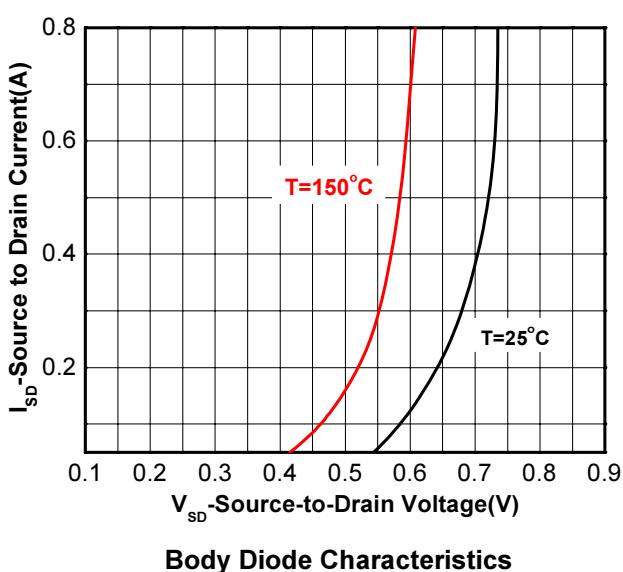
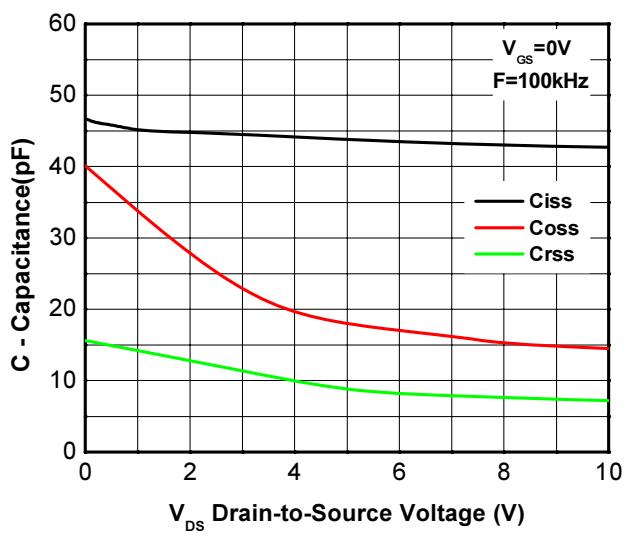
- a Surface mounted on FR4 Board using 1 square inch pad size, 1oz copper
- b Surface mounted on FR4 board using minimum pad size, 1oz copper
- c Repetitive rating, pulse width limited by junction temperature, $t_p=10\mu\text{s}$, Duty Cycle=1%
- d Repetitive rating, pulse width limited by junction temperature $T_J=150^\circ\text{C}$.

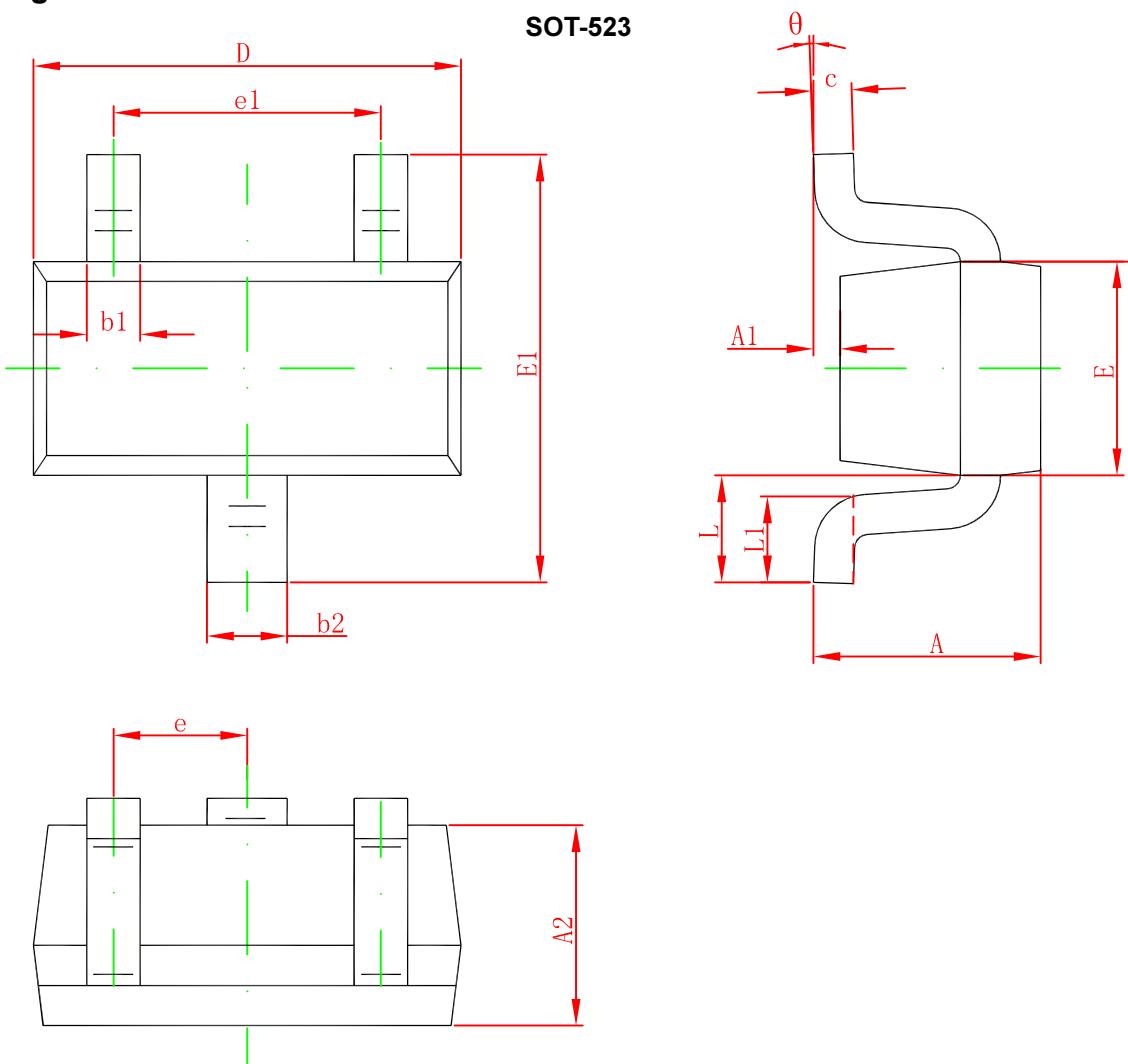
Electronics Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ.	Max	Unit
Off Characteristics						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=16\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
I_{GSS}	Gate –Source leakage current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 5\text{V}$			± 5	μA
ON Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	0.45	0.55	1.0	V
$R_{\text{DS}(\text{on})}$	Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}, I_D=0.55\text{A}$		220	310	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_D=0.45\text{A}$		260	360	$\text{m}\Omega$
		$V_{\text{GS}}=1.8\text{V}, I_D=0.35\text{A}$		320	460	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_D=0.4\text{A}$		1.0		S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=0\text{V}, f=100\text{kHz}$		68		pF
C_{oss}	Output Capacitance			9.0		pF
C_{rss}	Reverse Transfer Capacitance			7.5		pF
$Q_{\text{G}(\text{TOT})}$	Total Gate Charge	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=4.5\text{V}, I_D=0.55\text{A}$		1.15		nC
$Q_{\text{G}(\text{TH})}$	Threshold gate charge			0.06		nC
Q_{GS}	Gate-Source Charge			0.15		nC
Q_{GD}	Gate-Drain Charge			0.23		nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=10\text{V}, V_{\text{GS}}=4.5\text{V}, I_D=0.55\text{A}, R_G=6\Omega$		22		ns
t_{r}	Turn-On Rise Time			80		ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time			700		ns
t_{f}	Turn-Off Fall Time			380		ns
Body Diode Characteristics						
V_{SD}	Forward Diode Voltage	$V_{\text{GS}}=0\text{V}, I_S=0.35\text{A}$	0.5	0.7	1.5	V

Typical Performance Graph





Package Outline Dimensions
SOT-523


Symbol	Dimension in Millimeters	
	Min.	Max.
A	0.700	0.900
A1	0.000	0.100
A2	0.700	0.800
b1	0.150	0.250
b2	0.250	0.350
c	0.100	0.200
D	1.500	1.700
E	0.700	0.900
E1	1.450	1.750
e	0.500 Typ.	
e1	0.900	1.100
L	0.400 Ref.	
L1	0.260	0.460
θ	0°	8°