

## WL2803D

**Ultra low dropout, 800mA, CMOS LDO**

[Http://www.sh-willsemi.com](http://www.sh-willsemi.com)

### Descriptions

The WL2803D series are ultra low dropout, Low quiescent current, high PSRR CMOS LDO.

Using CMOS construction, the quiescent current consumed by the WL2803D is typically 150 $\mu$ A over the entire input voltage range, making it attractive for consumer, networking applications that demand high output current. The WL2803D series are available in wide output voltage range version from 1.2V to 3.3V.

The WL2803D series offer thermal shutdown (OTP) and current limit functions, to assure the stability of chip and power system at wrong condition, and it uses trimming technique to guarantee output voltage accuracy within  $\pm 2\%$ .

The WL2803D regulators are available in DFN2X2-6L packages. Standard products are Pb-free and Halogen-free.

### Features

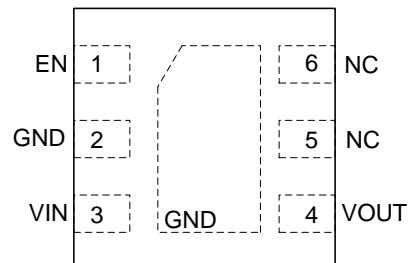
- Input voltage : 2.5V~5.5V
- Output voltage : 3.0V
- Output current : 800mA
- PSRR : 65dB @ 1KHz
- Dropout voltage : 130mV @  $I_{OUT}=0.5A$
- Output noise : 100 $\mu$ V
- Quiescent current : 150 $\mu$ A Typ.

### Applications

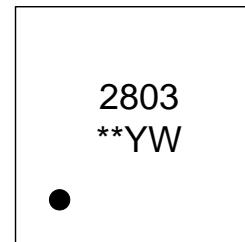
- LCD TV
- STB
- Computer, Graphic card
- Network communication equipments
- Others portable electronics devices



**DFN2X2-6L**



**Pin Configuration (Top View)**

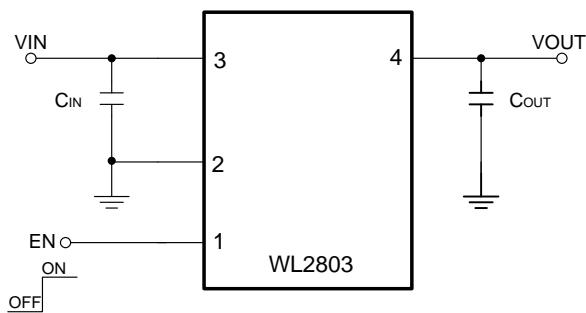


2803 = Device code  
 \*\* = Voltage code (30: 3.0V)  
 Y = Year code  
 W = Week code  
**Marking**

### Order Information

For detail information, Please refer to page 9.

### Typical Application

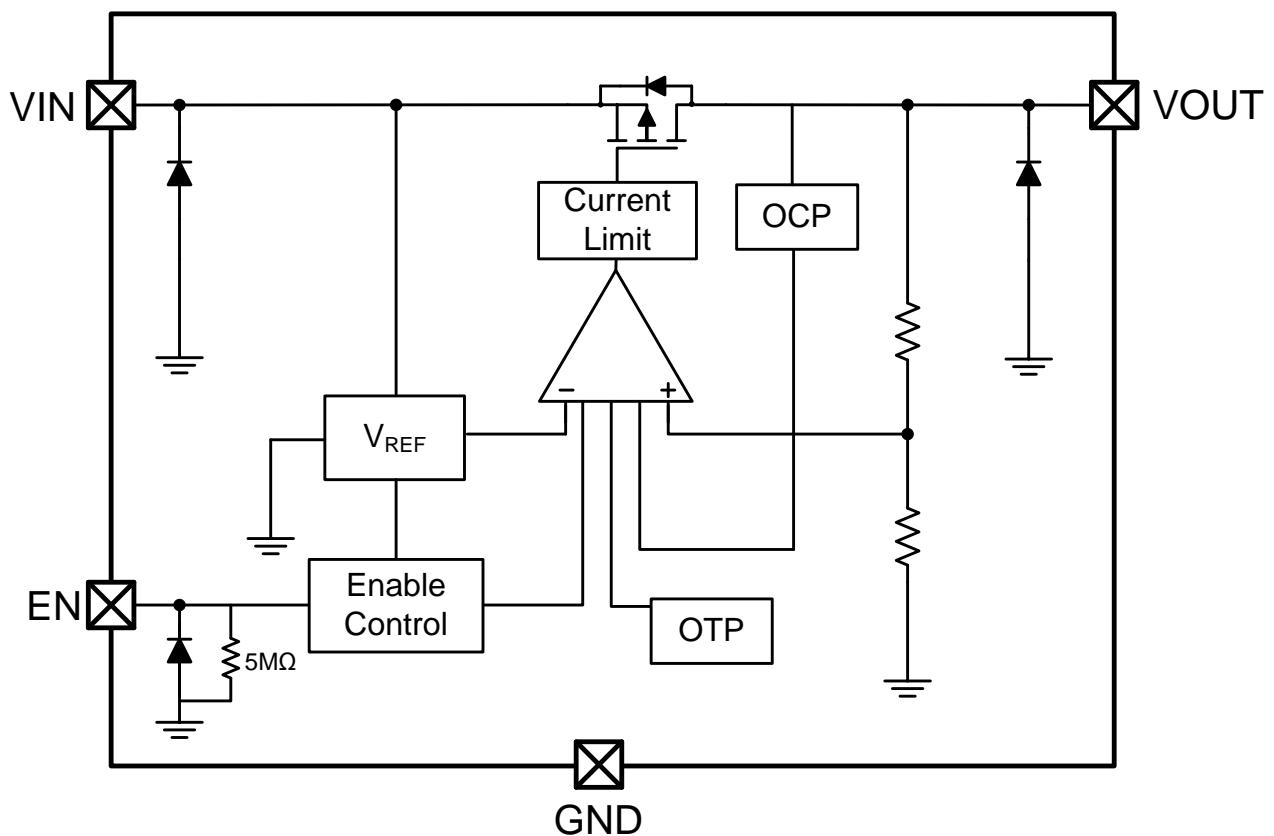


### Pin Description

PIN	Symbol	Description
1	EN	Enable, Active High
2	GND	Ground
3	VIN	Input
4	V <sub>OUT</sub>	Output
5,6	NC	Not connect

	Min.	Typ.	Max.
C <sub>IN</sub>	2.2uF	4.7uF	
C <sub>OUT</sub>	1uF	4.7uF	

### Block Diagram



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input voltage range	V <sub>IN</sub>	-0.3~6.5	V
Output voltage range	V <sub>OUT</sub>	-0.3~V <sub>IN</sub>	V
Power dissipation * <sup>1</sup> * <sup>3</sup>	P <sub>D</sub>	0.7	W
Power dissipation * <sup>2</sup> * <sup>3</sup>		0.5	W
Thermal resistance * <sup>2</sup>	R <sub>θJA</sub>	165	°C/W
Junction temperature	T <sub>J</sub>	150	°C
Lead temperature(10s)	T <sub>L</sub>	260	°C
Storage temperature	T <sub>stg</sub>	-55 ~ 150	°C
ESD Ratings	HBM	±8000	V
	MM	±400	V

**Note:** These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

\*<sup>1</sup>: Surface mounted on FR-4 Board using 1 square inch pad size, dual side, 1oz copper

\*<sup>2</sup>: Surface mounted on FR-4 board using minimum pad size, 1oz copper

\*<sup>3</sup>: Power dissipation is calculate by P<sub>D</sub> = (V<sub>IN</sub>-V<sub>OUT</sub>) x I<sub>OUT</sub>

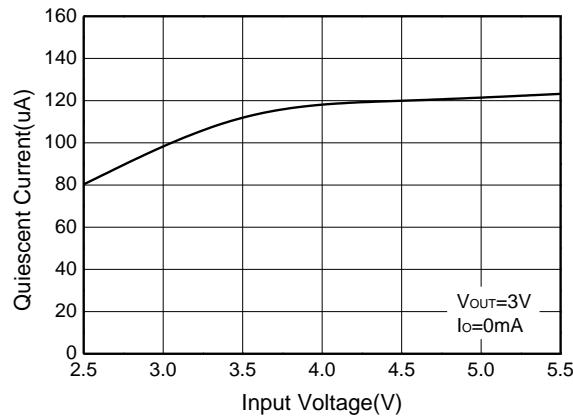
## Recommend Operating Ratings

Parameter	Symbol	Value	Unit
Operating Supply voltage	V <sub>IN</sub>	2.5~5.5	V
Operating Temperature Range	T <sub>opr</sub>	-40~85	°C

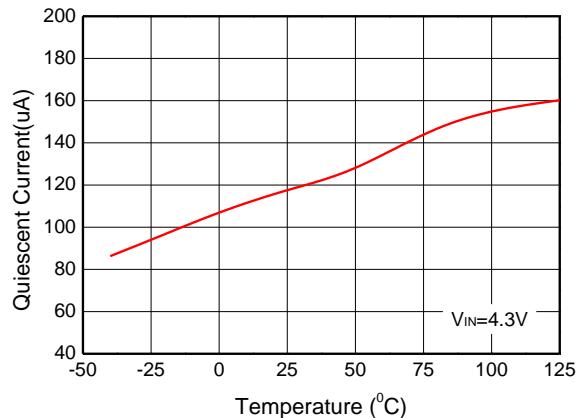
**Electronics Characteristics (Ta=25°C, V<sub>IN</sub>=V<sub>OUT</sub>+1V, C<sub>IN</sub>=C<sub>OUT</sub>=4.7μF, unless otherwise noted)**

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Output Voltage	V <sub>OUT</sub>	V <sub>OUT</sub> < 1.5V, V <sub>IN</sub> =2.5V, I <sub>OUT</sub> =1mA	V <sub>OUT</sub> - 30mV	V <sub>OUT</sub>	V <sub>OUT</sub> + 30mV	V
		V <sub>OUT</sub> ≥ 1.5V, I <sub>OUT</sub> =1mA	V <sub>OUT</sub> * 0.98	V <sub>OUT</sub>	V <sub>OUT</sub> * 1.02	
Dropout Voltage	V <sub>DROP</sub>	V <sub>OUT</sub> =V <sub>OUT</sub> *0.98, I <sub>OUT</sub> =1A		250	450	mV
Current Limit	I <sub>LIM</sub>	V <sub>IN</sub> =5V	1			A
Line Regulation	△V <sub>LINE</sub>	V <sub>OUT</sub> =3.3V, V <sub>IN</sub> =4.3~6.0V, I <sub>OUT</sub> =1mA		5	10	mV
Load Regulation	△V <sub>Load</sub>	V <sub>OUT</sub> =3.3V, I <sub>OUT</sub> =1~500mA		10	30	mV
Quiescent Current	I <sub>Q</sub>	V <sub>OUT</sub> =3.3V, I <sub>OUT</sub> =0		150	200	uA
Shut-down Current	I <sub>SHDN</sub>	V <sub>EN</sub> = 0V		0.1	1.0	uA
Power Supply Ripple Rejection	PSRR	V <sub>IN</sub> =(V <sub>OUT</sub> +1V) <sub>DC</sub> +0.2V <sub>P-P</sub> F=1KHz, I <sub>OUT</sub> =10mA		65		dB
		V <sub>IN</sub> =(V <sub>OUT</sub> +1V) <sub>DC</sub> +0.2V <sub>P-P</sub> F=10KHz, I <sub>OUT</sub> =10mA		58		
Output noise voltage	e <sub>NO</sub>	10Hz to 100KHz, C <sub>OUT</sub> =4.7μF		100		μV <sub>P-P</sub>
EN logic high voltage	V <sub>ENH</sub>	V <sub>IN</sub> =5.5V, I <sub>OUT</sub> =1mA	1.2			V
EN logic low voltage	V <sub>ENL</sub>	V <sub>IN</sub> =5.5V, I <sub>OUT</sub> =0mA			0.4	V
Thermal shutdown threshold	T <sub>SD</sub>			165		°C
Thermal shutdown hysteresis	△ T <sub>SD</sub>			30		°C

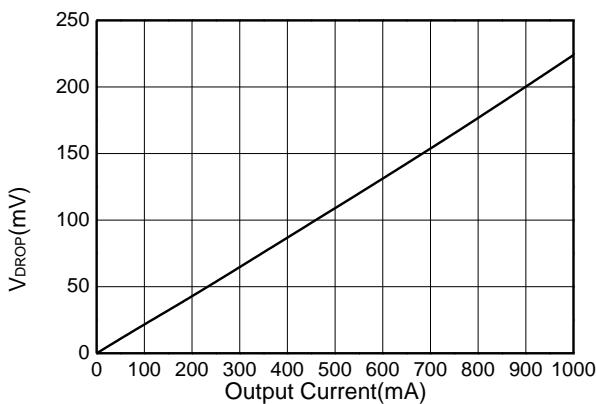
**Typical characteristics (Ta=25°C, V<sub>IN</sub>=V<sub>OUT</sub>+1V, C<sub>IN</sub>=C<sub>OUT</sub>=4.7μF, unless otherwise noted)**



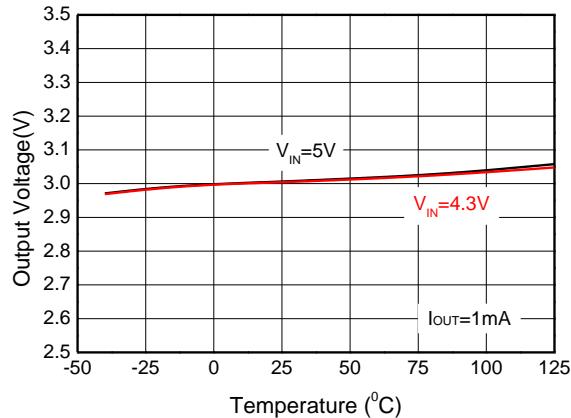
**Quiescent current vs. Supply voltage**



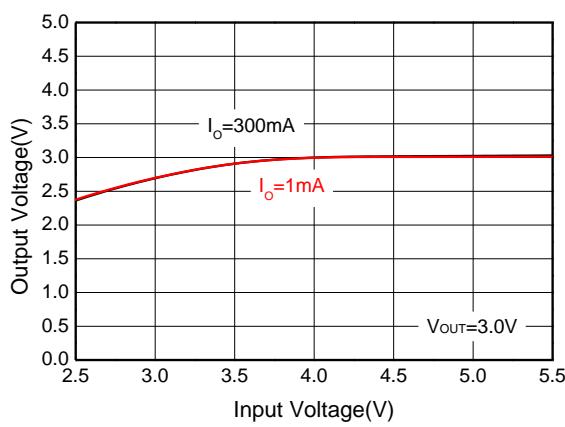
**Quiescent current vs. Temperature**



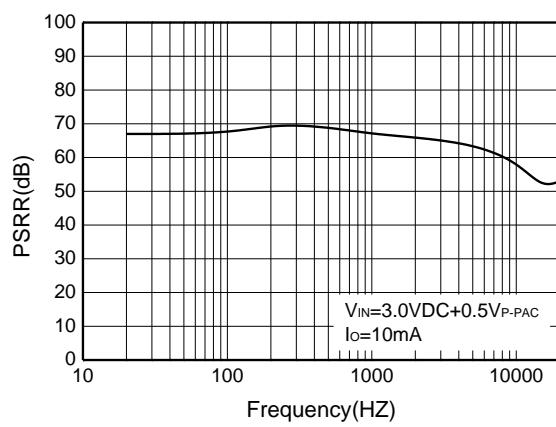
**DROP Voltage vs. Output Current**



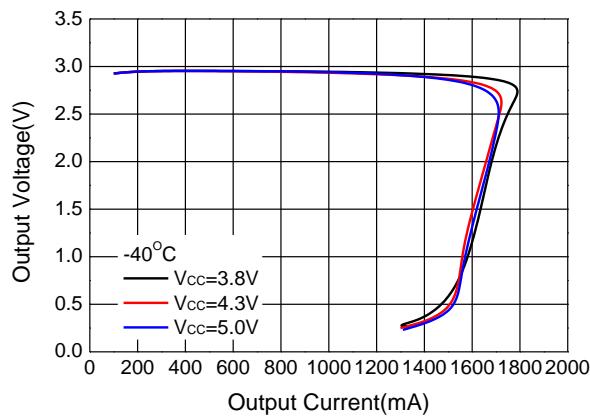
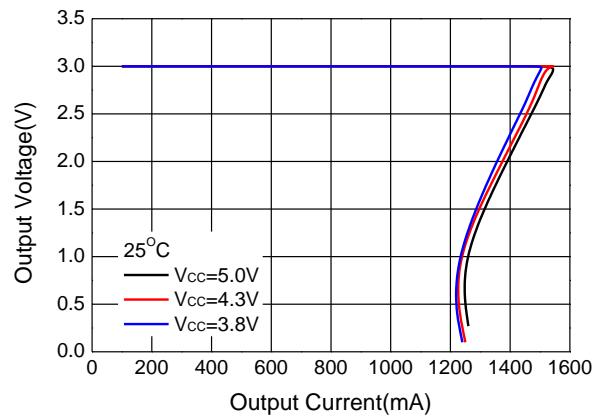
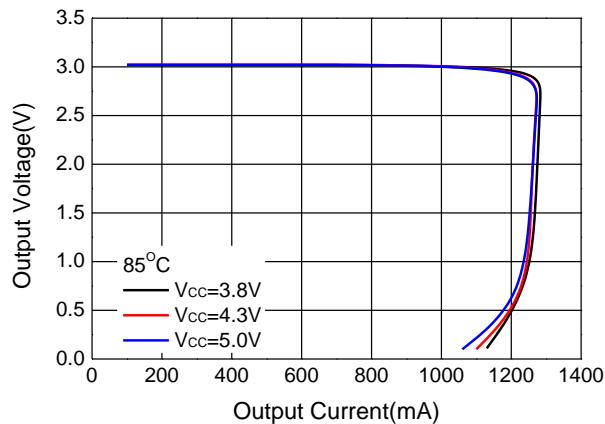
**Output Voltage vs. Temperature**

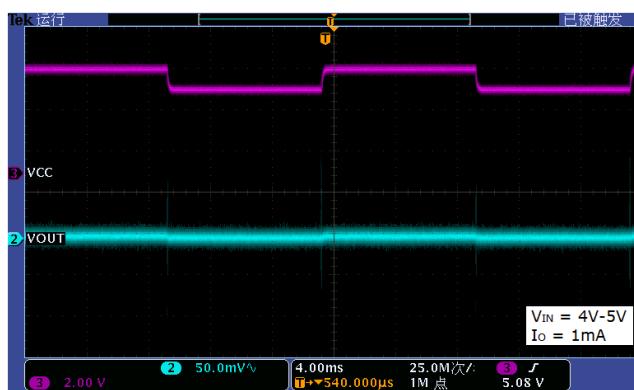
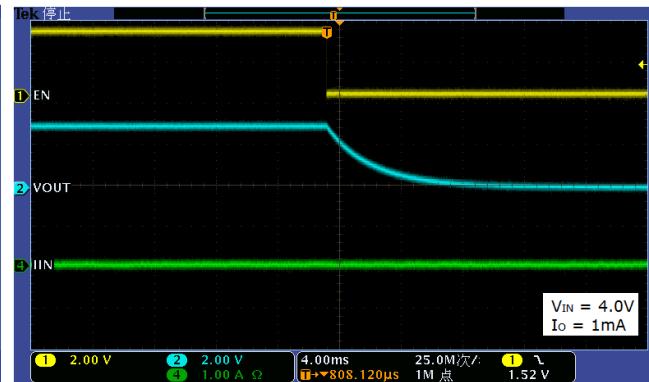


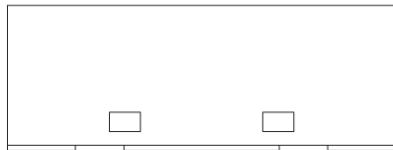
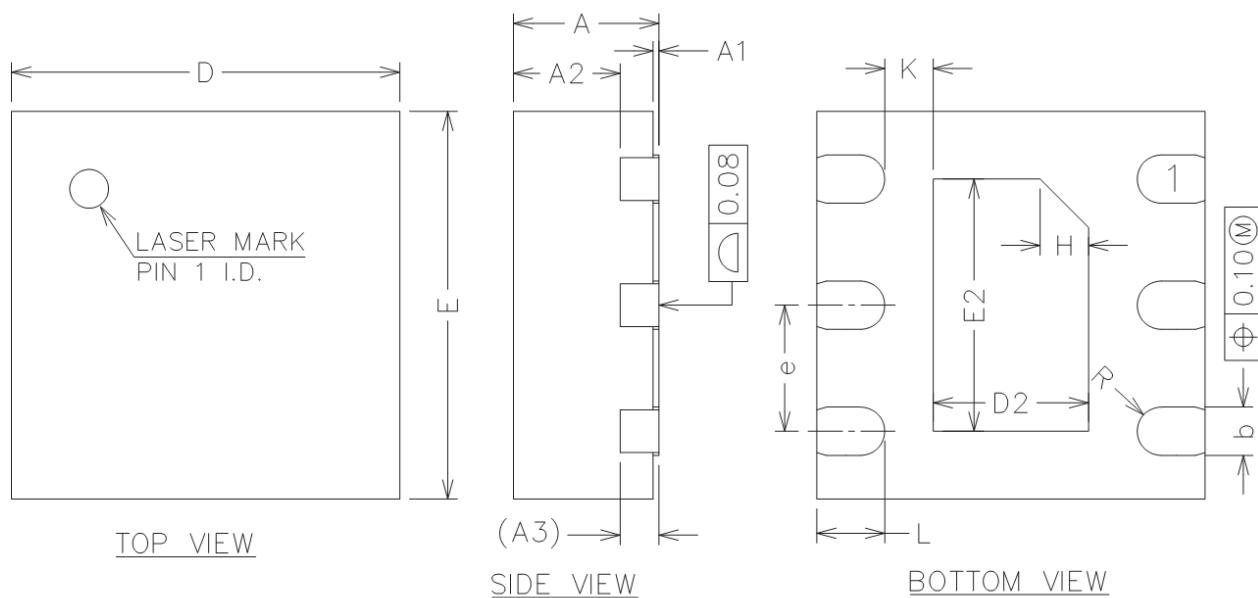
**Output voltage vs. Supply voltage**



**PSRR**


**Output voltage vs. Output current**

**Output voltage vs. Output current**

**Output voltage vs. Output current**



**Package outline dimensions**
**DFN2X2-6L**

SIDE VIEW

Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.700	0.750	0.800
A1	0.000	0.020	0.050
A2	0.500	0.550	0.600
A3	0.200 REF.		
b	0.200	0.250	0.300
D	1.900	2.000	2.100
E	1.900	2.000	2.100
D2	0.700	0.800	0.900
E2	1.200	1.300	1.400
e	0.550	0.650	0.750
H	0.250 REF.		
K	0.200	-	-
L	0.300	0.350	0.400
R	0.110	-	-

## ORDER INFORMATION

Ordering No.	V <sub>OUT</sub> (V)	Package	Marking	Operating Temperature	Shipping
WL2803D30-6/TR	3.0	DFN2X2-6L	2803/30YW	-40 ~ +85°C	3000/Tape and Reel